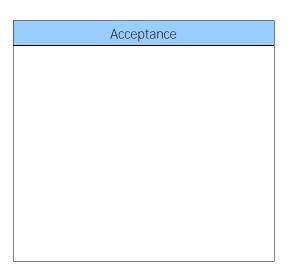
Product Specification							
Landmark	ark	Version	Issued Date	Page.			
Technology	Model: LM700-MU	2.1	2013.4.13	1 / 22			

## Thin-Film-Transistor LCD Module Model: LM700-MU



# Landmark Technology, Inc. 172 Component Drive

# San Jose, CA. 95131

Tel: 408 434-9302 Fax: 408 434-0954

Approved and Checked by

Approved by	Checked by	Made by

Landmark Technology, Inc.

## **REVISION RECORD**

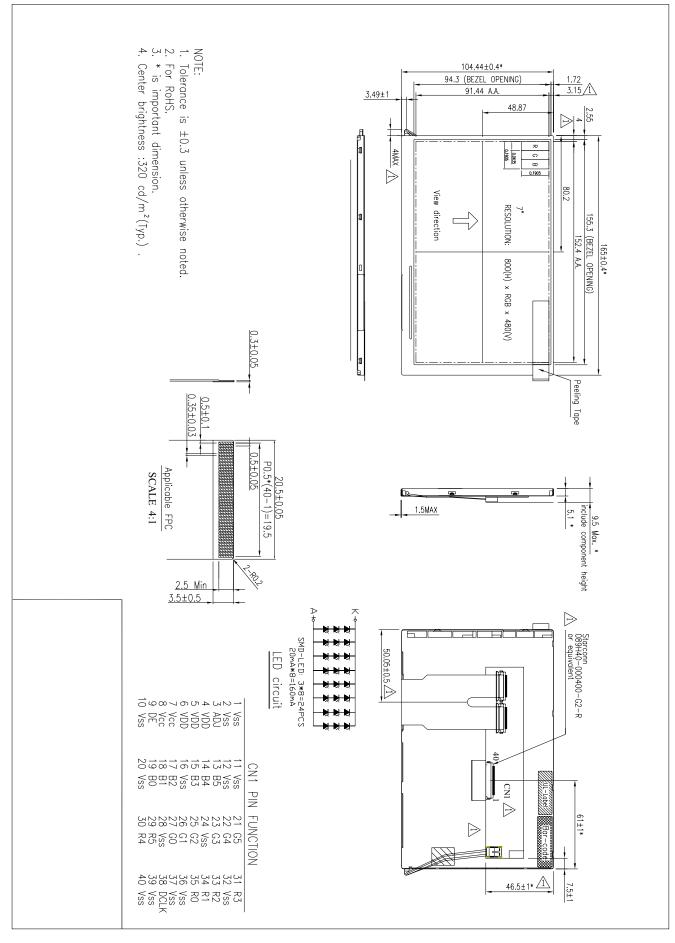
REV NO.	REV DATE	CONTENTS	REMARKS
1.0	2009-11-09	First Release	

# CONTENTS

- GENERAL INFORMATION
- EXTERNAL DIMENSIONS
- ABSOLUTE MAXIMUM RATINGS
- ELECTRICAL CHARACTERISTICS
- ELECTRO-OPTICAL CHARACTERISTICS
- INTERFACE DESCRIPTION
- APPLICATION NOTES
- RELIABILITY TEST
- INSPECTION CRITERION
- PRECAUTIONS FOR USING LCD MODULES
- PRIOR CONSULT MATTER

## ■ GENERAL INFORMATION

Item	Contents	Unit
LCD type	TFT/Normally white	/
Size	7.0	Inch
Viewing Direction	12:00	Clock
Gray scale inversion direction		
$LCM(W \times H \times D)$	165.00×104.44×9.50	mm <sup>3</sup>
Active area (W×H)	152.40×91.44	mm <sup>2</sup>
Dot pitch (W×H)	0.0635×0.1905	mm <sup>2</sup>
Number of dots	800 (RGB) × 480	/
Backlight type	24 LEDs	/
Interface type	RGB 18 bits	/
Color depth	262K	/
Pixel configuration	Stripe	/
Surface treatment		
Input voltage	3.3	V
With/Without TSP	With TSP	/
Weight	TBD	g



#### ■ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Symbol Min		Unit
Power supply voltage		VCC,VDD	-0.3	6.0	V
Logic input voltage		VI	-0.3	VCC+0.3	V
Operating temperature		Тор	-20	70	°C
Storage temperature		Тѕт	Tst -30 80		°C
Humidity Operation		20%~90% relative humidity (Typ 38°C)			RH
Humidity	Non operation	5%~90% relative humidity (Typ 38°C)			

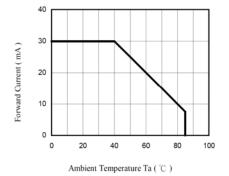
### **ELECTRICAL CHARACTERISTICS**

DC CHARACTERISTIC
-------------------

Parameter	Symbol	Min	Тур	Max	Unit
Power supply voltage for LCD	VCC	3.0	3.3	3.6	V
Power supply current for LCD	ICC	-	150	200	mA
Power supply voltage for LED	VDD	3.0	3.3	5.5	V
	IDD	-	650	850	mA
Power supply current for LED	IDD	-	400	550	IIIA
Input voltage ' H ' level	Vih	0.7VCC	-	VCC	V
Input voltage ' L ' level	VIL	0	-	0.3VCC	V
ADJ frequency		19K	20K	21K	Hz
ADI input voltage	Vih	3.0	-	3.3	V
ADJ input voltage	Vil	0	-	0.3	v
LED dice life time					

Note 1: The "LED dice life time" is defined as the brightness decrease to 50% original brightness that the ambient temperature is  $18^{\circ}C \sim 28^{\circ}C$  and LED dice current=20mA.

Note2: The LED Dice's Ambient Temp. vs. Allowable Forward Current Curve.



Item		Symbol	Condition	Min	Тур	Max	Unit	Remark	Note
Response time		Tr+Tf		-	16	26	ms	FIG 1.	4
Contrast r	atio	Cr	θ=0°	250	400	-		FIG 2.	1
Luminance uniformity		δ WHITE	Ø=0° Ta=25℃	70	80	-	%	FIG 2.	3
Surface Lum	inance	Lv				500	2	FIG 2.	2
			$\emptyset = 90^{\circ}$	55	60	-	deg	FIG 3.	
Viewing angl	a ranga	θ	$\emptyset = 270^{\circ}$	55	60	-	deg	FIG 3.	6
viewing angi	Viewing angle range		$\emptyset = 0^{\circ}$	65	70	-	deg	FIG 3.	0
			$\varnothing = 180^{\circ}$	65	70	-	deg	FIG 3.	
	Red	X		-	-	-			
	Kcu	у							
	Green	X	θ=0°						
CIE (x, y)		у	Ø=0°					FIG 2.	5
chromaticity	Blue	X	Ta=25℃					110 2.	5
	Dide	у	10 25 0						
	White	X		0.26	0.31	0.36			
		у		0.28	0.33	0.38			
		tis		-	-	2			

#### **■ELECTRO-OPTICAL CHARACTERISTICS**

Note 1. Contrast Ratio(CR) is defined mathematically as For more information see FIG 2.

Contrast Ratio = <u>Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)</u> Average Surface Luminance with all black pixels (P1, P2, P 3, P4, P5)

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

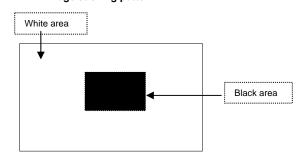
Lv = Average Surface Luminance with all white pixels (P1, P2, P 3, P4, P5)

Note 3. The uniformity in surface luminance  $, \delta$  WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance. For more information see FIG 2.

 $\delta$  WHITE = <u>Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)</u>

#### Note 8: Definition of Image sticking (tis):

Continuously display the test pattern shown in the figure below for 2 hours. Then display a completely white screen. The previous image shall not persist more than 2 sec at 25 °C Image sticking pattern



#### FIG. 1 The definition of Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

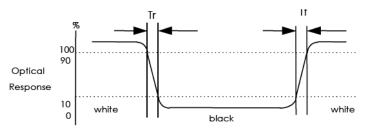
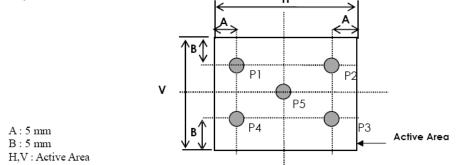
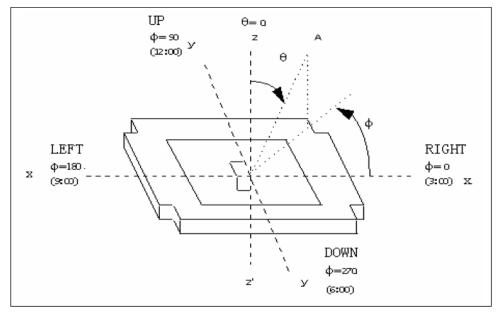


FIG. 2 Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity



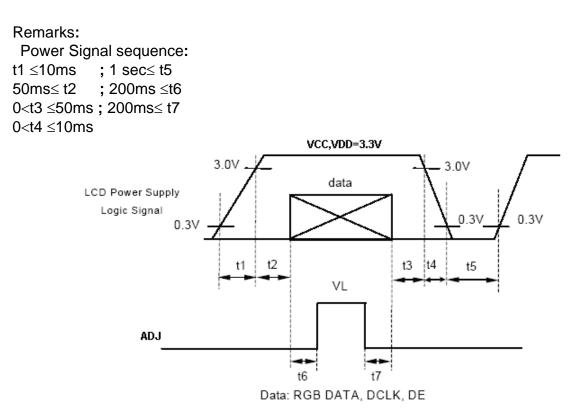
Light spot size  $\emptyset$ =7mm, 500mm distance from the LCD surfade to detector lens measurement instrument is TOPCON's luminance meter BM-5

#### FIG. 3 The definition of viewing angle



#### **INTERFACE DESCRIPTION**

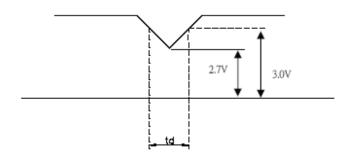
Pin NO.	SYMBOL	DESCRIPTION
1	Vss	Power Ground
2	Vss	Power Ground
3	ADJ	Brightness control for LED B/L
4	VDD	Power Supply for LED Driver
5	VDD	Power Supply for LED Driver
6	VDD	Power Supply for LED Driver
7	Vcc	Power Supply for Digital Circuit
8	Vcc	Power Supply for Digital Circuit
9	DE	Data Enable
10	Vss	Power Ground
11	Vss	Power Ground
12	Vss	Power Ground
13	B5	Blue Data 5 (MSB)
14	B4	Blue Data 4
15	B3	Blue Data 3
16	Vss	Power Ground
17	B2	Blue Data 2
18	B1	Blue Data 1
19	B0	Blue Data 0 (LSB)
20	Vss	Power Ground
21	G5	Green Data 5 (MSB)
22	G4	Green Data 4
23	G3	Green Data 3
24	Vss	Power Ground
25	G2	Green Data 2
26	G1	Green Data 1
27	G0	Green Data 0 (LSB)
28	Vss	Power Ground
29	R5	Red Data 5 (MSB)
30	R4	Red Data 4
31	R3	Red Data 3
32	Vss	Power Ground
33	R2	Red Data 2
34	R1	Red Data 1
35	R0	Red Data 0
36	Vss	Power Ground
37	Vss	Power Ground
38	DCLK	Clock Signals ; Latch Data at the Falling Edge
39	Vss	Power Ground
40	Vss	Power Ground



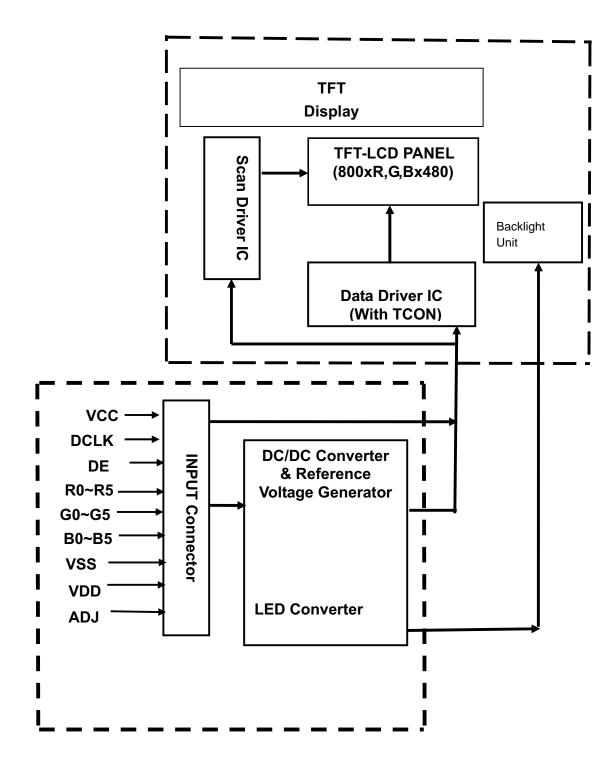
VCC, VDD -dip condition:

(1) 2.7V  $\leq$ VCC,VDD $\leq$ 3.0V: td  $\leq$ 10 ms

(2) VCC,VDD>3.0V: VCC,VDD -dip condition should be the same with VCC,VDD-turn-on condition.



#### **BLOCK DIAGRAM**



### ■ APPLICATION NOTES

#### **1. AC Characteristics**

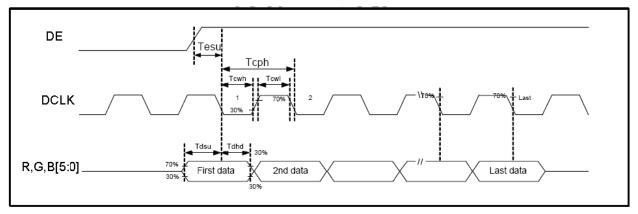
**1.1 AC Electrical Characteristics** 

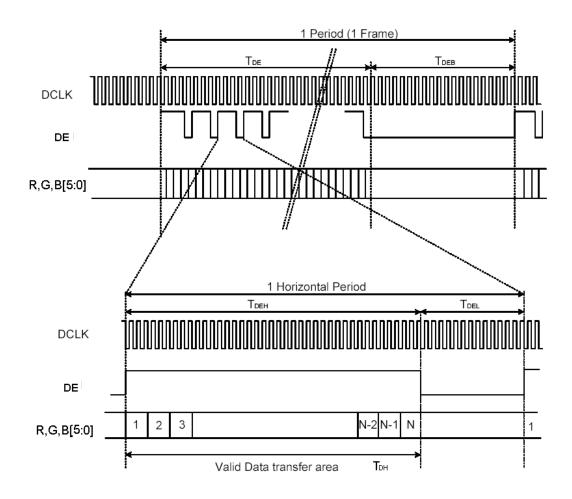
Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Data setup time	Tdsu	6	-	-	ns
Data hold time	Tdhd	6	-	-	ns
DE setup time	Tesu	6	-	-	ns

#### 1.2 Resolution:800 x 480

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
DCLK frequency	Fсрн	-	33.26	-	MHz
DCLK period	Тсрн	-	30.06	-	ns
DCLK pulse duty	Тсwн	40	50	60	%
DE period	TDEH+TDEL	1000	1056	1200	Тсрн
DE pulse width	Тден	-	800	-	Тсрн
DE frame blanking	Тдев	10	45	110	Tdeh+Tdel
DE frame width	TDE	-	480	-	Tdeh+Tdel

#### 2. Timing Controller Timing Chart 2.1 Clock and Data input waveforms





## 3. Color Data Input Assignment

									Da	ita S	Jigi	a							
				Red Green										Blue					
Color			R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Colors	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
of Red	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(0)/ Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Gray Scale	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
of Green	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	0	0	0 0	0	0 0	1 1	1 1	1 1	1	1	0	0 0	0	0	0 0	0	0
	Green(63) Blue(0)/ Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue (1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue (2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Gray Scale	· ·	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
of	•	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Blue	Blue (61)	0	0	0	0	0	0	0	0	0	0	0	0	· 1	1	1	· 1	0	1
	Blue (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

#### Correspondence between Data and Display Position

S0001	S0002	S0003	S0004	S0005	S0006	S0007	S0008		S2399	S2400
R001	G001	B001	R002	G002	B002	R003	G003		G800	B800
	I			I	I	I		I		.
Dood	0004	<b>D</b> 004	DOOO	0000	D000	D000	0000		G800	B800
F	R001	R001 G001	R001 G001 B001	R001 G001 B001 R002	R001 G001 B001 R002 G002	R001 G001 B001 R002 G002 B002	R001 G001 B001 R002 G002 B002 R003	R001 G001 B001 R002 G002 B002 R003 G003	R001 G001 B001 R002 G002 B002 R003 G003	R001 G001 B001 R002 G002 B002 R003 G003 G800

No.	Test Item	Test Condition	Inspection after test	
1	High Temperature Storage	$80\pm2^{\circ}C/240$ hours		
2	Low Temperature Storage	$-30\pm2^{\circ}C/240$ hours	Inspection after 2~4hours storage at room temperature, the comple shall be free from	
3	High Temperature Operating	$70\pm2^{\circ}C/240$ hours	<ul> <li>the sample shall be free from defects:</li> <li>1.Air bubble in the LCD;</li> </ul>	
4	Low Temperature Operating	$-20\pm2^{\circ}C/240$ hours	2.Sealleak; 3.Non-display;	
5	Temperature Cycle storage	$-30\pm2^{\circ}C\sim25\sim80\pm2^{\circ}C\times100$ cycles (30 min.) (5 min.) (30 min.)	4.missing segments; 5.Glass crack;	
6	Damp proof Test operating	$40^{\circ}\text{C} \pm 5^{\circ}\text{C} \times 90\%$ RH/240 hours	6.Current Idd is twice higher than initial value.	
7	Vibration Test	Frequency: 0~55Hz Amplitude:1.5mm		
		6 cycles for each direction of X.Y.Z		

Remark:

1. The test samples should be applied to only one test item.

2.Sample size for each test item is 5~10pcs.

3.For Damp Proof Test, Pure water(Resistance>10M $\Omega$ ) should be used.

4.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.

5.EL evaluation should be excepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.

6. Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

OUTGOING QUALITY STANDARD	PAGE 1 OF 6
TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA	

This specification is made to be used as the standard acceptance/rejection criteria for Wider Screen TFT-LCD module product.

#### 1. Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC Z1.4-1993, normal level 2 and based on:

Major defect: AQL 0.65

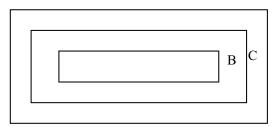
Minor defect: AQL 1.5

#### 2. Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of 20~40W light intensity, all directions for inspecting the sample should be within 45° against perpendicular line. All inspection ND3% use.

#### **3.** Definition of Inspection Item.

3.1 Definition of inspection zone in LCD.



Zone A: character/Digit area

Zone B: viewing area except Zone A (ZoneA+ZoneB=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

ZoneB+ZoneC= Around opaque <u>edge</u> area on TP.

Fig.1 Inspection zones in an LCD.

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

#### **3.2 Definition of some visual defect**

Bright dot.	Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern.
Dark dot.	Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue picture, or pure whiter picture.
Dark / Bright Lines.	Lines on display which appear dark/bright and usually result from the contamination.

#### PAGE 2 OF 6

#### TITLE: FUNCTIONAL TEST & INSPECTION CRITERIA

#### 4. Major Defect

Item No	Items to be inspected	Inspection Standard	Classification of defects
4.1	All functional defects	<ol> <li>No display</li> <li>Display abnormally</li> <li>Open or missing segment</li> <li>Short circuit</li> <li>Excess power consumption</li> <li>Back-light no lighting, flickering and abnormal lighting.</li> </ol>	
4.2	Missing	Missing component	Major
4.3	Outline dimension	Overall outline dimension beyond the drawing is not allowed.	
4.4	Crack	Creaks tend to break are not allowed.	

#### 5. Minor Defect

Item No	Items to be inspected		Classification of defects				
	Bright dot. defect.	Zone Size(mm)		A	cceptable	Qty	
		Size(iiiii)		А	В	С	
5.1	↓ O↓y	Φ≤0.15		cceptable ( of spot not a		g Acceptable	
	$\Phi = (x+y)/2$	0.15<Φ≤0.25		N≤	5.		
		$0.25 < \Phi \leq 0.50$		N≤2			
							Minor
		Zone		A	cceptable	e Q'ty	
		Size(mm)	$\square$	А	В	С	
5.2	Dark dot defect.	Φ≤0.15		Accept	able		
		$0.15 < \Phi \leqslant 0.3$	0	N≤	6	Acceptable	
		$0.30 < \Phi \leqslant 0.$	50	N≤	<b>4</b>		
5.3	Bright / Dark line.	$0.01 < W \leq 0.10,$ $N \leq 1$		$0.30 < L \le$	≤ 1.50,	Acceptable	
2 3	. Minimum d	ve dots shall not exce istance between def dark sub pixel defec .: Length, N: Count	fective fective	ve dots is			han 1pair.

#### PAGE 3 OF 6

#### TITLE: FUNCTIONAL TEST & INSPECTION CRITERIA

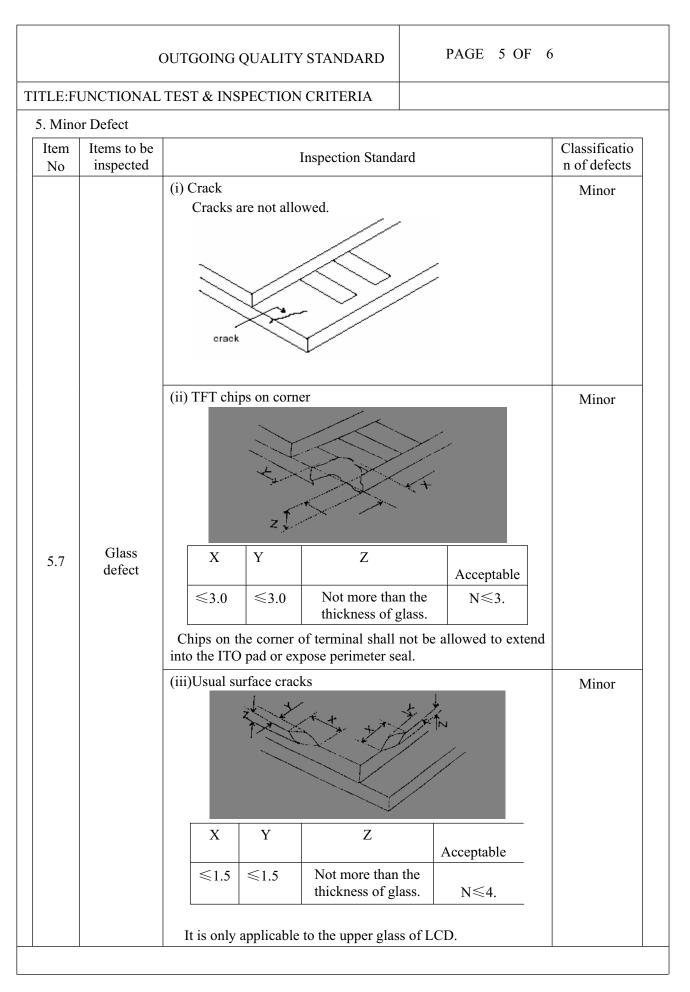
Item No	Items to be inspected		Classification of defects						
	Linear defect	Siz	ze(m)	m) Acceptable Qty					
	Foreign material under polarizer,	L(Length)	W(Width)	A	Zone	С			
		Ignore	W≤0.05		otable				
		L≤5.0	0.05 <w≤0.15< td=""><td>N</td><td>ໄ≪5</td><td>Acceptable</td><td></td></w≤0.15<>	N	ໄ≪5	Acceptable			
<i>с</i> 4		5.0≪L	0.15≤W		0	ole	_		
5.4	Circular Defect,						Minor		
	Foreign material under polarizer,	Zor	ne A	cceptabl	e Q'ty				
	y y	Size(mm)	A	В		C			
		Φ≤0.25	Accep	able					
	<b>≪</b> x→	$0.25 < \Phi \le 0.5$	50 N≤	4	Acce	ptable			
	$\Phi = (x+y)/2$	0.50≤Φ	0						
5.5	Polarizer defect.	dimension (ii) Incomplete is not allo 5.4.2 Dirt on po	n position should e covering of the vi owed. larizer can be wiped eas Nick & Dent	ewing are	ea due to sl ld be acce le Qty	hifting	Minor		
				1 1					
		Φ<0.2	5 Accept	able	-				
		$\Phi < 0.23$ $0.25 \leqslant \Phi \leqslant 0$	-		Accepta	ble			

#### PAGE 4 OF 6

#### TITLE: FUNCTIONAL TEST & INSPECTION CRITERIA

5. Minor Defect

Item No	Items to be inspected		Classification of defects				
		5.4.4Air bubble	Minor				
		Size(1	mm)	Zo			
			A	В	С		
		Φ<		ceptable	_		
		0.3<0		3	- Acceptable		
		1.0<4		1	_		
		Φ>	1.5	0			
5.6	Polarizer defect	assemb the line (ii) If the non-ope judge b	Polarizer scratch ling or in the op defect of 5.4. Polarizer scrato	erating co ch can b n or som Accep	seen after cover ondition, judge by pe seen only ir ne special angle ptable Qty Zone		
			w (widui)	A B	C		
		Ignore	W≤0.02	Ignore			
		1.0 <l≤5.0< td=""><td>0.02<w≤0.2< td=""><td>N≪4.</td><td>Ignore</td><td></td></w≤0.2<></td></l≤5.0<>	0.02 <w≤0.2< td=""><td>N≪4.</td><td>Ignore</td><td></td></w≤0.2<>	N≪4.	Ignore		
		5.0 <l< td=""><td>0.2<w< td=""><td>0</td><td></td><td></td></w<></td></l<>	0.2 <w< td=""><td>0</td><td></td><td></td></w<>	0			



PAGE 6 OF 6

#### TITLE: FUNCTIONAL TEST & INSPECTION CRITERIA

Item No	Items to be inspected	Inspection Standard	Classification of defects					
1	Difference in Spec.	None allowed	Major					
2	Pattern peeling	No substrate pattern peeling and floating						
3	Soldering defects	No soldering missing No soldering bridge No cold soldering	Major Major Minor					
4	Resist flaw on Printed Circuit Boards	visible copper foil (Ø0.5mm or more) on substrate pattern.	Minor					
5	Accretion of metallic Foreign matter	No accretion of metallic foreign matters (Not exceed $\emptyset$ 0.2mm).	Minor Minor					
6	Stain	No stain to spoil cosmetic badly.	Minor					
7	Plate discoloring	No plate fading, rusting and discoloring.	Minor					
8	Solder amount 1. Lead parts	a. Soldering side of PCB Solder to form a 'Filet' all around the lead. Solder should not hide the lead form perfectly. (too much) b. Components side ( In case of 'Through Hole PCB')	Minor					
	2. Flat packages	Solder to reach the Components side of PCB.         Either 'Toe' (A) or 'Seal' (B) of	Minor					
		the lead to be covered by 'Filet'						
	3. Chips	$(3/2) H \ge h \ge (1/2) H$	Minor					
9	Solder ball/Solder splash	a. The spacing between solder ball and the conductor or solder pad $h \ge 0.13$ mm. The diameter of solder ball d $\le 0.15$ mm. b. The quantity of solder balls or $\uparrow$ h	Minor					
		solder. Splashes isn't beyond 5 $\bigcirc$	Minor					
		c.Solder balls/Solder splashes do not violate minimum electrical clearance. d.Solder balls/Solder splashes must be entrapped /	Major Minor					
		encapsulated or attached to the metal surface . Note: Entrapped/encapsulated/attached is intended to mean that normal service environment of the product will not cause a solder ball to become dislodged.						

#### 2 Handling precaution for LCM

- 2.1 LCM is easy to be damaged. Please note below and be careful for handling.
- 2.2 Correct handling:





As above picture, please handle with anti-static gloves around LCM edges.

#### 2.3 Incorrect handling:



Please don't touch IC directly.



Please don't hold the surface of panel.



Please don't hold the surface of IC.



Please don't stack LCM.



Please don't stretch interface of output, such as FPC cable.



Please don't operate with sharp stick such as pens.

#### **3** Storage Precautions

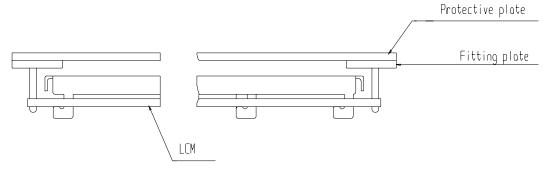
- 3.1 When storing the LCD modules, the following precaution are necessary.
  - 3.1.1 Store them in a sealed polyethylene bag. If properly sealed, there is no need for the desiccant.
  - 3.1.2 Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.
  - 3.1.3 The polarizer surface should not come in contact with any other objects (We advise you to store them in the anti-static electricity container in which they were shipped).
- 3.2 Others 其它
  - 3.2.1 Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.
  - 3.2.2 If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
  - 3.2.3 To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.
    - 3.2.3.1 Exposed area of the printed circuit board.
    - 3.2.3.2 -Terminal electrode sections.

#### 4 USING LCD MODULES

4.1 Installing LCD Modules

The hole in the printed circuit board is used to fix LCM as shown in the picture below. Attend to the following items when installing the LCM.

4.1.1 Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



## **4.3** Precaution for soldering the LCM

	Manual soldering	Machine drag soldering	Machine press soldering
No RoHS	290°C ~350°C.	330°C ~350°C.	300°C ~330°C.
Product	Time : 3-5S.	Speed : 15-17 mm/s.	Time : 3-6S.
Flouuet			Press: 0.8~1.2Mpa
RoHS	340°C ~370°C.	350°C ~370°C.	330°C ~360°C.
Product	Time : 3-5S.	Speed : 15-17 mm/s.	Time : 3-6S.
Tioduct			Press: 0.8~1.2Mpa